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Page 2 of 4 APPLICATION NO. ATTY. DOCKET NO. 10/809,838 1875.138000G **FORM PTO-1449** INVENTOR Arya R. Behzad INFORMATION DISCLOSURE STATEMENT **ART UNIT** FILING DATE March 26, 2004 2832 **U.S. PATENT DOCUMENTS** EXAMINER INITIAL **DOCUMENT NUMBER** DATE NAME CLASS SUB-CLASS | FILING DATE 07/1991 AA2 5,031,233 Ragan AB2 5,077,541 12/1991 Gilbert 10/1992 Rubens AC2 5,157,350 AD2 5,329,143 07/1994 Chan et al. 06/1995 Osburn et al. AE2 5,428,829 08/1995 AF2 5,446,311 Ewen et al. 33/ AG2 5.483.207 01/1996 Gabara 2B Z 12/1996 Linder et al. AH2 5,581,213 330 AI2 5,587,688 12/1996 Mulbrook 330 **FOREIGN PATENT DOCUMENTS EXAMINER** CLASS **DOCUMENT NUMBER** DATE COUNTRY **TRANSLATION** INITIAL SUB-CLASS Yes AJ2 0 431 887 06/1991 EΡ No Yes 04/1993 AK2 0 535 536 A1 EP No Yes AL2 0 601 560 A1 06/1994 EP No Yes 06/1997 EΡ 0 780 853 A1 AM2 No OTHER (Including Author, Title, Date, Pertinent Pages, etc.) Craninckx et al., "A 1.8-Ghz Low-Phase-Noise CMOS VCO Using Optimized Hollow Spiral Inductors," ΑN 2 IEEE Journal of Solid-State Circuits, Vol. 32, No. 5, May 1997, pp. 736-744. Long et al., "The Modeling Characterization, and Design of Monolithic Inductors for Silicon RF IC's," IEEE Journal of Solid-State Circuits, Vol. 32, No. 3, March 1997, pp. 357-369. AO 2 Yue et al., "On-Chip Spiral Inductors with Patterned Ground Shields for Si-Based RF IC's," IEEE ΑP 2 Journal of Solid-State Circuits, Vol. 33, No. 5, May 1998, pp. 743-751. U.S. Patent Application Ser. No. 09/573,356, Khorramabadi, filed Mar. 17, 2000. AQ 2 2 **EXAMINER** DATE CONSIDERED EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance

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